ET-AVR ISP

ET-AVR ISP which is a Board is designed for download HEX File into MCU AVR family from Atmel by using Serial Programming and we can download this Hex File through Parallel Port of computer. We use this 10P board with ET-CAP from ETTand Software PonyProg2000 that is Program Download HEX File into CPU AVR family by using Serial Programming uses with ET-AVR ISP. It can use with ETT Board AVR family well. The method for using this program, user can learn from HELP of program by self, in this case, we would like to introduce only method to setup program PonyProg2000 to use with all ETT Board AVR family.

Board Structure of ET-AVR ISP



- No.1 is Port for connecting with ET-CAP 10P from ETT. It uses to program HEX File into MCU.
- No.2 is LED PGM (GREEN). It use to display status of proramming and downloading Hex File into MCU.
- No.3 is LED PWR (RED). It uses to display status of Power Supply on board.
- No.4 is Port for connecting with board Target. It can program Hex File into board ET-AVR STAMP Atmega64 by inserting board ET-AVR ISP into port of ET-PSPI and feature of Pin arrangement is in the picture.



Pin Position	Signal's name	
1	MOSI	
2	VCC	
3	Not use	
4,6,8,10	GND	
5	RESET	
7	SCK	
9	MISO	

To Interface Devices for Programming Hex File

To program Code Hex File into AVR MCU, we use with ET-CAB10PIN and Program PonyProg2000 by connecting ET-CAP10PIN with Printer Port. Select Jumper to use program PonyProg2000, interface Download cable in point of ET-PSPI Download of board and then supply electricity into board completely. If there's any devices are connected from external at Port PB, user need to take off first. The feature of connection is in the picture.



(Left) ET-CAP10P V2.0 (Right) ET-CAP10P V1.0 Picture displays Jumper selection and Download Cable connection of ET-CAP10P for using with AVR. ET-CAP10P

(Left) Upper View

(Right) Side View

Picture displays the connection between ET-AVR ISP and ET-AVR SATMP ATmega64. To interface both boards, the position of triangle symbol in one board need to coincide with other one board.

Programming Board ET-AVR STAMP ATMEGA64 with program PonyProg2000

Program PonyProg2000 which is Program Download File into CPU AVR family by using HEX Serial Programming can use with ETT Board AVR family well. The method for using this program, user can learn from HELP of program by self, in this case, we would like to introduce only method to setup program PonyProg2000 to use with ETT Board AVR family. In case of using CPU AVR family No.ATmega64, user should be careful because there's various Fuse Bits for assignment CPU operation inside structure of ATmega64. These Fuse Bits effect on Serial programming download because if user select Fuse bit incorrectly, may not be use Serial programming

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download until modify Fuse Bit of CPU correctly with Parallel Program first.

If using program PonyProg2000 to program CPU AVR family into ETT Board, user need to assign Option of program as followig.

1. Assign Setup \rightarrow Interface Setup... as following.

I/O port setup			
I/O port setup			
C Serial	 Parallel 		
SI Prog API	Avr ISP I/0		
С СОМ1 С СОМ3 С СОМ2 С СОМ4	LPT1 C LPT3 C LPT2		
Select Polarity of the Control lines Invert Reset Invert D-IN Invert SCKL Invert D-OUT Cancel OK Probe			

- Select I/O Port as Parallel and select mode programming as Ave ISP I/O.
- Select truely Printer Port. For example, select LPT1 in case of using with Printer Port LPT1.
- Do not select Polarity Control Line.
- Setup program only one time when use it in the fisrt time.

2. Run program PonyProg2000 to calculate the suitable speed for sending signal into program CPU. Can select from Setup \rightarrow Calibration.

Yes or	No
?	Bus timing calibration. Be sure there are no application running other than PonyProg2000 (the CPU and hard disk have to be idle) The calibration may take a couple of seconds. Do you want to run calibration now?
Yes	No Cancel

Notice	
\bigtriangleup	Calibration OK
OK	

- Run Calibration only one time when use this program in the first time.

3. Select CPU No. from **Device** \rightarrow **AVR Micro** \rightarrow such as **Atmega64**.

PonyProg2000 - Serial Device F	Programmer	
File Edit Device Command Script U	Jtility Setup ? Window	
1 2 2 2 1 9 9 9	🚺 🗊 🗐 🔍 💡 🔦 🗛 🗛 🕅	▼ ATmega64 ▼
	2 8 8 0 3	
🕸 LY\74HC595_OUT_7SEG\74H	IC595_OUT_7SEG.hex	<u>^</u>
0000000 00 C0 0F EF 0D B 0000100 02 B9 F0 E0 EE E 0000200 03 D0 2E D0 31 9 0000300 C2 9A C1 9A C1 9A 00000300 C2 9A C1 9A C1 9A 00000400 4A EF 00 0B 4A 9 00000700 08 95 5F 93 58 E 00000000 5B 4F 66 6D 7D 2 00000000 FF FF FF FF FF F 00000000 FF FF FF FF FF F 00000000 FF FF FF </td <td>BF 00 E1 - 0E BF 06 E0 07 BB 00 E8 E8 C8 95 - 00 20 D9 F3 00 94 00 2D 96 F7 CF - 20 2F 18 E0 C2 98 27 FD 98 22 1F - 1A 95 C1 F7 1F 9A 1F 98 93 30 E1 - 4A EF 00 00 4A 95 E9 F7 91 3F 91 - 88 95 3F 93 34F 93 30 EF 91 3F 91 - 88 95 SF 93 34F 93 30 EF 91 3F 91 - 3A 95 D1 F7 4F 91 35 91 92 25 27 7F 67 - 30 00 <t< td=""><td>. n. e. J. u. J. u. J. . uo t H H fe a. " . z. z. z d - / . t z. '. z. v. v. '. e v d. . e? 6 6 0 J. J. e d J. e. J. e d J. e d e d J. e. J. e d fe d e d e d . e d fe d e d e d e d . e d fe d e d e d . e d fe d e d e d . e d e e d e d . e d e d e d . e d e d e d . e d e d . e d e d e d e d e d . e d e d e d e d . e d e d e d e d e d . e d e d e d e d e d . e d e d e d e d e d e d e d e d</td></t<></td>	BF 00 E1 - 0E BF 06 E0 07 BB 00 E8 E8 C8 95 - 00 20 D9 F3 00 94 00 2D 96 F7 CF - 20 2F 18 E0 C2 98 27 FD 98 22 1F - 1A 95 C1 F7 1F 9A 1F 98 93 30 E1 - 4A EF 00 00 4A 95 E9 F7 91 3F 91 - 88 95 3F 93 34F 93 30 EF 91 3F 91 - 88 95 SF 93 34F 93 30 EF 91 3F 91 - 3A 95 D1 F7 4F 91 35 91 92 25 27 7F 67 - 30 00 <t< td=""><td>. n. e. J. u. J. u. J. . uo t H H fe a. " . z. z. z d - / . t z. '. z. v. v. '. e v d. . e? 6 6 0 J. J. e d J. e. J. e d J. e d e d J. e. J. e d fe d e d e d . e d fe d e d e d e d . e d fe d e d e d . e d fe d e d e d . e d e e d e d . e d e d e d . e d e d e d . e d e d . e d e d e d e d e d . e d e d e d e d . e d e d e d e d e d . e d e d e d e d e d . e d e d e d e d e d e d e d e d</td></t<>	. n. e. J. u. J. u. J. . uo t H H fe a. " . z. z. z d - / . t z. '. z. v. v. '. e v d. . e? 6 6 0 J. J. e d J. e. J. e d J. e d e d J. e. J. e d fe d e d e d . e d fe d e d e d e d . e d fe d e d e d . e d fe d e d e d . e d e e d e d . e d e d e d . e d e d e d . e d e d . e d e d e d e d e d . e d e d e d e d . e d e d e d e d e d . e d e d e d e d e d . e d e d e d e d e d e d e d e d
<	· · · · · · · · · · · · · · · · · · ·	>
PonyProg2000 ATmega64 Si	ize 67584 Bytes CRC AE67h	

4. Assign Command \rightarrow Security and Configuration Bits. If it is other AVR No., user can assign operation of Fuse Bit as required and can learn details of Fuse Bit from Data Sheet of CPU by self. In case of using with ATmega64, user should be careful and need to assign Fuse Bit correctly because if user assign Fuse Bit incorrectly, it can not run program CPU by using Serial programming. User can assign as following.



Configuration and Security bits
7 🗖 6 🗖 BootLock12 🗖 BootLock11 🗖 BootLock02 🗖 BootLock01 🗖 Lock2 🗖 Lock1
□ 7 □ 6 □ 5 □ 4 □ 3 □ 2 □ M103C □ WDTON
🔽 OCDEN 🗹 JTAGEN 🗹 SPIEN 🔽 CKOPT 🗖 EESAVE 🗐 BOOTSZ1 🗐 BOOTSZ0 🗐 BOOTRST
🗖 BODLEVEL 🗖 BODEN 🗖 SUT1 🔽 SUT0 🧮 CKSEL3 🧮 CKSEL2 🗐 CKSEL1 🗐 CKSEL0
Checked items means programmed (bit = 0)
Refer to device datasheet, please
Cancel OK Clear All Set All Write Read

Picture displays Fuse Bit assignment for using with CPU No. Atmega64.

The Fuse Bits's Meaning of ATmega64

- □ In case of selecting $[\sqrt{}]$ in front of any Fuse Bit. It means that assign Fuse Bit as "0" or runs program Fuse Bit.
- In case of not selecting [√] in front of any Fuse Bit. It means that assign Fus Bit as "1" or not run program Fuse Bit.

The Fuse Bits's Meaning of ATmega64 that effects on Serial Programming

- SPIEN is Serial programming Enable Bit. User to assign this function always need for downloading Program into CPU by In-System Serial programming. Normally, this Fuse Bit is assigned from mamufacturer, so user can not modify them with Mode erase or Serial programming. If CPU is programmed by Parallel programming, user does not remember to program thes Fuse Bit again.
- OCDEN and JTAGEN both Bit use in case of debugging MCU operation and programming through JTAG Interface and it need to use with AVR JTAG

Debuger. If user do not use this function, it is not necessary to select both bits.

- **CKOPT** is Oscillator Option Bit. If user selects this program Fuse Bit, it means that assignment CPU run with frequency at 16MHz. If do not select this Fuse Bit, it means that assignment CPU run with frequency not greater than 8MHz. If using with ETT standard Board, it uses XTAL as Frequency Generator, so user need to run default value of this Fuse Bit and CPU can run with XTAL Frequency between 1.0MHz-16.0MHz.
- **CKSEL3...0** is Select Clock Source Bit. It uses together for selecting generator and frequency for using with CPU. In case of using with ETT standard Board, user need to select as External Crystal 1.0 MHz - 16.0 MHZ but if user select other default values, program will run The important thing that user incorrectly. should remember, if selecting as External Clock or External RC Oscillator, it makes CPU can not run. Because user does not interface clock from external , so it can not run program CPU by using Serial Programming. So, user need to modify CPU to correct Fuse Bit and select clock as External Crystal.

Clock Generator of AVR Atmega64	Fuse Bit assignment of CKSEL[30] (0=Program,1=Un-Program)	
External Crystal/Ceramic Resonator	1111-1010	
External Low Frequency Crystal	1001	
External RC Oscillator	1000-0101	
Calibrated Internal RC Oscillator	0100-0001	
External Clock	0000	

Table shows the selection of Frequency Generator from Fuse Bit CKSEL [3...0].

NOTICE

- Default value as 1, it means that not assign program Fuse Bit to run and not put $[\sqrt{}]$ in front of any Fuse Bit.
- Default value as 0, it means that assign program Fuse Bit to run and put $[\sqrt{}]$ in front of Fuse Bit.
- Should assign program Fuse Bit of CKOPT to run at frequency as 1.0MHz-16.00MHz.
- Do not run program Fuse Bit of CKSEL[3..0] because its operation will run incorrectly. For example, if user assign all program Fuse Bit of CKSEL[3..0] as 0, CPU does not run after program PonyProg2000 wrote Fuse Bit into CPU completely. It makes CPU cannot use with ETT board and can not run program to modify new Fuse Bit into CPU by using Serial Programming. To assign all Fuse Bit of CKSEL[3..0] as 0, it means that CPU runs with External Clock and Frequency Generator of External Crystal stops running and CPU will wait for External Clock only. For ETT Board uses only External Crystal. When CPU can not run, we can not modify any Fuse Bit by using Serial Programming. So, user need to modify CPU by Parallel Program first and after that we can use Serial Programming.
- Fuse Bit Position of Lock[2..1], user can assign as required.

5. Assign Command \rightarrow Program Option as following.



6. Open File for programming into CPU by selecting from File \rightarrow Open Program (FLASH) File... \rightarrow and then specify name and address of HEX File completely.

New Window	
Open Device File	
Open Program (FLASH) File	
Open Data (EEPROM) File	
Save Device File	
Save Device File As	
Save Program (FLASH) File As	
Save Data (EEPROM) File As	
Reload Files	Ctrl-L
Print	
Close	
Exit	
74HC595_OUT_75EG.hex PROG	
Main.hex PROG	

Open program	(FLASH) content	t file			? 🗙
Look in:	24HC595_OU	T_7SEG	• +	E 💣 🎟 -	
📁 Recent	■ 74HC595_OUT_	_7SEG			
Desktop					
My Documents					
My Computer					
					
My Network Places	File name:	74HC595_OUT_7SEG		•	Open
	Files of type:	*.hex		•	Cancel

7. Start to program data into CPU by selecting from Command \rightarrow Program and after that program starts to run as instructions in No.5 as following; Load File \rightarrow Erase \rightarrow Write Program memory (FLASH) and Write Security and Config Bits respectively and user need to wait for program run completely.

Status	
Verifying	
25%	
Abort	
\downarrow	
Notice	
<u>/</u>]}	
ОК	

After programmed completely, CPU starts to run as downloaded data in program.

The basic verification when can not download any program.

If there's some error in step of programming, user can verify problems as following;

- Read User Manual of Board and User Manual of Method to download program AVR with PonyProg2000 well.
- Check all cable connection and download program by usig PonyProg2000, user need to use with Cable Download ET-CAP10PIN from ETT and need to assign Jumper as truly PonyProg (See all details on page 2).
- Check power supply of board.
- Check all default values of program for downloading PonyProg2000.
- Check signal from PORT-PB of CPU is connected with devices from external while downloading data or not. For example, it is connected with LED or signal from PORT PB is connected with other circuits while downloading data.